

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10001478
	Filing Date		2001-11-01
	First Named Inventor	Craig Nemecek	
	Art Unit	2123	
	Examiner Name	Jason Scott Proctor	
	Attorney Docket Number	CD01213M	

U.S. PATENTS						
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	1	9532478	WO	A1	1995-11-30	Klien	Entire Document	<input type="checkbox"/>
	2	404083405	JP	A1	1992-03-17	Yasue	Entire Document	<input type="checkbox"/>
	3	404095408	JP	A1	2002-03-27	Nishimaki	Entire Document	<input type="checkbox"/>

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4	405055842	JP	A1	1993-03-26	Kondo	Entire Document	<input type="checkbox"/>
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NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T ⁵
	1	USPTO Application Number 09/922,579 (CD00230): "A Method for a Efficient Supply to a Microcontroller," Kutz et al., filed on 08/03/2001; 37 pages	<input type="checkbox"/>
	2	USPTO Application Number 09/935,454 (CD00194): "Method and Apparatus for Local and Global Power Management in a Programmable Analog Circuit," Monte Mar, filed on 08/22/2001; 51 pages	<input type="checkbox"/>

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1	BURSKY, "FPGA Combines Multiple Interfaces and Logic," Electronic Design, Vol. 48 No. 20, pp.74-78 (2 October 2000); 5 pages	<input type="checkbox"/>
2	ANONYMOUS, "Warp Nine Engineering - The IEEE 1284 Experts-F/Port Product Sheet," undated web page found at http://www.fapo.com/fport.htm ; printed on 4/12/2005; 2 pages	<input type="checkbox"/>
3	ANONYMOUS, "F/Port:Fast Parallel Port for the PC" Installation Manual" Release 7.1, circa 1997, available for download from http://www.fapo.com/fport.htm ; 25 pages	<input type="checkbox"/>

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1	USPTO Non-Final Rejection for 09/998,848 (CD01177M) dated 12/21/04; 14 pages	<input type="checkbox"/>
2	USPTO Final Rejection for 09/989,767 (CD01178M) dated 01/11/07; 12 pages	<input type="checkbox"/>
3	"In-Circuit Emulators - descriptions of the major ICEs around"; retrieved on November 14, 2005 from http://www.algonet.se/~staffann/developer/emulator.htm ; 6 pages	<input type="checkbox"/>
4	USPTO Application Number 09/975,104 (CD00183): "Capturing Test/Emulation and Enabling Real-Time Debugging Using FPGA for In-Circuit Emulation," Warren Snyder, filed on 10/10/2001; 35 pages	<input type="checkbox"/>
5	USPTO Final Rejection for 09/989,767 (CD01178M) dated 01/15/09; 21 pages	<input type="checkbox"/>
6	USPTO Final Rejection for 09/989,767 (CD01178M) dated 03/06/06; 15 pages	<input type="checkbox"/>
7	USPTO Final Rejection for 09/989,767 (CD01178M) dated 04/06/05; 13 pages	<input type="checkbox"/>
8	USPTO Final Rejection for 09/989,767 (CD01178M) dated 12/27/07; 21 pages	<input type="checkbox"/>
9	USPTO Application Number 09/975,338 (CD00186): "Method for Breaking Execution of a Test Code in DUT and Emulator Chip Essentially Simultaneously and Handling Complex Breakpoint Events," Nemecek et al., filed on 10/10/2001; 34 pages	<input type="checkbox"/>
10	USPTO Application Number 09/975,030 (CD00185): "Emulator Chip-Board Architecture for Interface," Snyder et al., filed on 10/10/2001; 37 pages	<input type="checkbox"/>
11	USPTO Application Number 09/975,338 (CD00186): "Method for Breaking Execution of a Test Code in DUT and Emulator Chip Essentially Simultaneously and Handling Complex Breakpoint Events," Nemecek et al., filed on 10/10/2001; 34 pages	<input type="checkbox"/>

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1	USPTO Application Number 10/803,030 (CD00169C): "Programmable Microcontrollable Architecture (Mixed Analog/Digital)," Snyder et al., filed on 03/16/2004; 13 pages	<input type="checkbox"/>
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1	USPTO Requirement for Restriction/Election for Application Number 09/972,003 (CD00176) dated 05/06/2004; 4 pages	<input type="checkbox"/>
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1	USPTO Notice of Allowance for Application Number 10/011,214 (CD00191) dated 04/11/2005; 4 pages	<input type="checkbox"/>
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1	USPTO Application Number 09/989,808 (CD01169M): "Automatic generation of application program interfaces, source code, interrupts, and data sheets for microcontroller programming," Bartz et al., filed on 11/19/2001; 67 pages	<input type="checkbox"/>
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1	USPTO Notice of Allowance for Application Number 11/986,338 (CD1197C) dated 05/07/2009; 1 page	<input type="checkbox"/>
2	USPTO Application Number 11/965,291 (CD2007130): "Universal Digital Block Interconnection and Channel Routing," Snyder et al., filed on 12/27/2007; 31 pages	<input type="checkbox"/>
3	USPTO Notice of Allowance for Application Number 11/965,291 (CD2007130) dated 05/05/2009; 7 pages	<input type="checkbox"/>
4	USPTO Non-Final Rejection for Application Number 11/965,291 (CD2007130) dated 12/17/2008; 8 pages	<input type="checkbox"/>
5	USPTO Application Number 11/986,338 (CD1197C): Reconfigurable Testing System and Method," Pleis et al., filed on 11/20/2007; 41 pages	<input type="checkbox"/>
6	USPTO Application Number 10/172,670 (CD01242M): "Method and System for Programming a Memory Device," Snyder et al.; filed on 06/13/2002; 66 pages	<input type="checkbox"/>
7	USPTO Notice of Allowance for Application Number 10/653,050 (CD01242C) dated 07/29/2004; 3 pages	<input type="checkbox"/>
8	Written Opinion of the International Search Authority for International Application Number PCT/US08/60680 (CD2007130PCT) dated 08/15/2008; 4 pages	<input type="checkbox"/>
9	USPTO Notice of Allowance for Application Number 09/475,879 (CD99047) dated 10/22/2004; 7 pages	<input type="checkbox"/>
10	USPTO Advisory Action for Application Number 09/475,879 (CD99047) dated 03/04/2002; 3 pages	<input type="checkbox"/>
11	USPTO Advisory Action for Application Number 09/475,879 (CD99047) dated 12/31/2001; 3 pages	<input type="checkbox"/>

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12	USPTO Final Rejection for Application Number 09/475,879 (CD99047) dated 10/11/2001; 10 pages	<input type="checkbox"/>
13	USPTO Non-Final Rejection for Application Number 09/475,879 (CD99047) dated 03/08/2001; 6 pages	<input type="checkbox"/>
14	USPTO Application Number 09/475,879 (CD99047): "Programmable Logic Device," Lacey et al.; filed on 12/30/1999; 50 pages	<input type="checkbox"/>
15	USPTO Application Number 09/475,808 (CD99069): "Configurable Memory for Programmable Logic Circuits," Lacey et al.; filed on 12/30/1999; 24 pages	<input type="checkbox"/>
16	USPTO Non-Final Rejection for Application Number 09/475,808 (CD99069) dated 06/06/2001; 8 pages	<input type="checkbox"/>
17	USPTO Notice of Allowance for Application Number 09/475,808 (CD99069) dated 11/06/2001; 8 pages	<input type="checkbox"/>
18	USPTO Application Number 10/137,497 (CD01197M): "Reconfigurable Testing System and Method," Pleis et al.; filed on 05/01/2002; 40 pages	<input type="checkbox"/>
19	USPTO Non-Final Rejection for Application Number 10/137,497 (CD01197M) dated 11/05/2004; 17 pages	<input type="checkbox"/>
20	USPTO Final Rejection for Application Number 10/137,497 (CD01197M) dated 05/05/2005; 13 pages	<input type="checkbox"/>
21	USPTO Non-Final Rejection for Application Number 10/137,497 (CD01197M) dated 09/22/2005; 21 pages	<input type="checkbox"/>
22	USPTO Final Rejection for Application Number 10/137,497 (CD01197M) dated 03/13/2006; 15 pages	<input type="checkbox"/>

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23	USPTO Non-Final Rejection for Application Number 10/137,497 (CD01197M) dated 08/02/2006; 21 pages	<input type="checkbox"/>
24	USPTO Notice of Allowance for Application Number 10/137,497 (CD01197M) dated 01/24/2007; 12 pages	<input type="checkbox"/>
25	USPTO Notice of Allowance for Application Number 10/137,497 (CD01197M) dated 07/20/2007; 4 pages	<input type="checkbox"/>
26	USPTO Application Number 10/653,050 (CD01242C): "Method and System for Programming a Memory Device," Snyder et al.; filed on 08/29/2003; 69 pages	<input type="checkbox"/>
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1	USPTO Requirement for Restriction for Application Number 11/985,340 (CD04036C1) dated 03/16/2009; 7 pages	<input type="checkbox"/>
2	USPTO Notice of Allowance for Application Number 11/985,340 (CD04036C1) dated 06/02/2009; 7 pages	<input type="checkbox"/>
3	USPTO Application Number 11/985,340 (CD04036C1): "Method and Circuit for Rapid Alignment of Signals," Moyal et al., filed on 11/13/2007; 34 pages	<input type="checkbox"/>
4	USPTO Non-Final Rejection for Application Number 11/088,028 (CD04036) dated 06/16/2006; 8 pages	<input type="checkbox"/>
5	USPTO Notice of Allowance for Application Number 11/088,028 (CD04036) dated 01/26/2007; 6 pages	<input type="checkbox"/>
6	USPTO Notice of Allowance for Application Number 11/088,028 (CD04036) dated 07/02/2007; 6 pages	<input type="checkbox"/>
7	USPTO Application Number 11/088,028 (CD04036): "Method and Circuit for Rapid Alignment of Signals," Moyal et al., filed on 11/13/2007; 34 pages	<input type="checkbox"/>
8	USPTO Notice of Allowance for Application Number 10/024,093 (CD99069C) dated 09/10/2002; 7 pages	<input type="checkbox"/>
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2	USPTO Notice of Allowance for Application Number 10/033,027 (CD00232M) dated 09/02/2009; 4 pages	<input type="checkbox"/>
3	USPTO Non-Final Rejection for Application Number 10/001,478 (CD01213M) dated 08/04/2009; 17 pages	<input type="checkbox"/>
4	USPTO Advisory Action for Application Number 10/001,478 (CD01213M) dated 06/30/2009; 3 pages	<input type="checkbox"/>
5	USPTO Final Rejection for Application Number 09/989,767 (CD01178) dated 12/07/2009; 22 pages	<input type="checkbox"/>
6	USPTO Notice of Allowance for Application Number 10/002,217 (CD01207M) dated 09/17/2009; 6 pages	<input type="checkbox"/>
7	USPTO Notice of Allowance for Application Number 10/002,217 (CD01207M) dated 06/08/2009; 8 pages	<input type="checkbox"/>
8	USPTO Notice of Allowance for Application Number 10/001,477 (CD01203M) dated 12/04/2009; 7 pages	<input type="checkbox"/>
9	USPTO Notice of Allowance for Application Number 10/001,477 (CD01203M) dated 08/26/2009; 6 pages	<input type="checkbox"/>
10	USPTO Ex Parte Quayle Action for Application Number 09/975,115 (CD00182) dated 08/20/2009; 7 pages	<input type="checkbox"/>
11	USPTO Non-Final Rejection for Application Number 09/994,601 (CD01170M) dated 07/09/2009; 11 pages	<input type="checkbox"/>

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12	USPTO Non-Final Rejection for Application Number 11/818,005 (CD01171C) dated 11/23/2009; 8 pages	<input type="checkbox"/>
13	USPTO Requirement for Restriction/Election for Application Number 11/818,005 (CD01171C) dated 07/14/2009; 5 pages	<input type="checkbox"/>
14	USPTO Advisory Action for Application Number 09/989,778 (CD01172M) dated 06/17/2009; 3 pages	<input type="checkbox"/>
15	USPTO Notice of Allowance for Application Number 09/994,600 (CD01174M) dated 08/25/2009; 4 pages	<input type="checkbox"/>
16	USPTO Final Rejection for Application Number 11/850,260 (CD2007006) dated 08/21/2009; 9 pages	<input type="checkbox"/>
17	USPTO Notice of Allowance for Application Number 09/989,762 (CD1175M) dated 10/30/2009; 6 pages	<input type="checkbox"/>
18	USPTO Notice of Allowance for Application Number 09/989,762 (CD1175M) dated 07/16/2009; 6 pages	<input type="checkbox"/>
19	USPTO Non-Final Rejection for Application Number 09/998,848 (CD01177M) dated 05/12/2009; 16 pages	<input type="checkbox"/>
20	USPTO Non-Final Rejection for Application Number 09/989,767 (CD01178) dated 05/12/2009; 21 pages	<input type="checkbox"/>
21	USPTO Final Rejection for Application Number 09/989,771 (CD01182M) dated 11/25/2009; 12 pages	<input type="checkbox"/>
22	USPTO Notice of Allowance for Application Number 09/989,765 (CD01194M) dated 09/03/2009; 7 pages	<input type="checkbox"/>

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25	USPTO Notice of Allowance for Application Number 11/986,338 (CD01197C) dated 10/19/2009; 4 pages	<input type="checkbox"/>
26	USPTO Notice of Allowance for Application Number 09/989,777 (CD01208M) dated 08/06/2009; 8 pages	<input type="checkbox"/>
27	USPTO Notice of Allowance for Application Number 10/008,096 (CD01212M) dated 10/21/2009; 6 pages	<input type="checkbox"/>
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29	USPTO Notice of Allowance for Application Number 11/985,340 (CD04036C1) dated 11/09/2009; 7 pages	<input type="checkbox"/>
30	USPTO Advisory Action for Application Number 11/201,627 (CD04109) dated 09/21/2009; 3 pages	<input type="checkbox"/>
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32	USPTO Miscellaneous Action for Application Number 11/201,922 (CD04111) dated 10/01/2009; 2 pages	<input type="checkbox"/>
33	USPTO Non-Final Rejection for Application Number 11/200,619 (CD04112) dated 06/17/2009; 12 pages	<input type="checkbox"/>

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34	USPTO Non-Final Rejection for Application Number 11/166,622 (CD05027) dated 09/29/2009; 11 pages	<input type="checkbox"/>
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36	USPTO Final Rejection for Application Number 11/166,622 (CD05027) dated 03/10/2009; 11 pages	<input type="checkbox"/>
37	USPTO Notice of Allowance for Application Number 12/218,404 (CD05028C1) dated 11/03/2009; 8 pages	<input type="checkbox"/>
38	USPTO Notice of Allowance for Application Number 12/218,404 (CD05028C1) dated 07/10/2009; 7 pages	<input type="checkbox"/>
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40	USPTO Non-Final Rejection for Application Number 11/698,660 (CD05176) dated 10/07/2009; 12 pages	<input type="checkbox"/>
41	USPTO Advisory Action for Application Number 11/698,660 (CD05176) dated 07/31/2009; 3 pages	<input type="checkbox"/>
42	USPTO Notice of Allowance for Application Number 11/983,291 (CD05191D) dated 10/22/2009; 7 pages	<input type="checkbox"/>
43	USPTO Final Rejection for Application Number 11/983,291 (CD05191D) dated 08/12/2009; 10 pages	<input type="checkbox"/>
44	USPTO Final Rejection for Application Number 11/322,044 (CD05198) dated 10/19/2009; 10 pages	<input type="checkbox"/>

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Application Number	10001478
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First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01213M

45	USPTO Final Rejection for Application Number 11/644,100 (CD05204) dated 08/19/2009; 12 pages	<input type="checkbox"/>
46	USPTO Advisory Action for Application Number 11/850,260 (CD2007006) dated 11/02/2009; 3 pages	<input type="checkbox"/>

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1	USPTO Final Rejection for Application Number 11/698,660 (CD05176) dated 02/16/2010; 14 pages	<input type="checkbox"/>
2	USPTO Notice of Allowance for Application Number 09/989,765 (CD01194M) dated 12/22/2009; 8 pages	<input type="checkbox"/>
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13	USPTO Final Rejection for Application Number 11/200,619 (CD04112) dated 01/04/2010; 18 pages	<input type="checkbox"/>
14	USPTO Non-Final Rejection for Application Number 11/644,100 (CD05204) dated 12/16/2009; 13 pages	<input type="checkbox"/>
15	USPTO Non-Final Rejection for Application Number 11/850,260 (CD2007006) dated 01/14/2010; 8 pages	<input type="checkbox"/>
16	USPTO Notice of Allowance for Application Number 10/033,027 (CD00232M) dated 02/18/2010; 4 pages	<input type="checkbox"/>
17	USPTO Notice of Allowance for Application Number 09/989,762 (CD01175M) dated 02/22/2010; 7 pages	<input type="checkbox"/>
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19	USPTO Non-Final Rejection for Application Number 12/136,557 (CD01210C) dated 03/15/2010; 10 pages	<input type="checkbox"/>
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23	USPTO Notice of Allowance for Application Number 11/709,866 (CD05170) dated 02/16/2010; 7 pages	<input type="checkbox"/>
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	Examiner Name	Jason Scott Proctor	
	Attorney Docket Number	CD01213M	

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1	USPTO Notice of Allowance for Application Number 10/001,477 (CD01203) dated 03/23/2010; 6 pages	<input type="checkbox"/>
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26	USPTO Advisory Action for Application Number 09/989,570 (CD01167M) dated 08/14/2003; 3 pages	<input type="checkbox"/>
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32	USPTO Non-Final Rejection for Application Number 09/989,571 (CD01168M) dated 07/12/2004; 9 pages	<input type="checkbox"/>
33	M. Mooris Mano, "Computer System Architecture," 1982, Prentice-Hall, 2nd Edition, pp. 261-264 and 435-440; 14 pages	<input type="checkbox"/>

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34	Dirk Killat, "A One-Chip Solution for Electronic Ballasts in Fluorescent Lamps," Power Electronics, < http://powerelectronics.com/mag/power_onechip_solution_electronic/ >, dated 03/01/2004, accessed 09/13/2005; 4 pages	<input type="checkbox"/>
35	Fred Eady, "PSoC 101," Circuit Cellar, August 2004, accessed 09/13/2005, < http://www.circuitcellar.com/library/print/0804/eady169/2.htm >; 4 pages	<input type="checkbox"/>
36	Vixel, "InSpeed SOC 320 Embedded Storage Switch," 2003, Vixel, pp. 1-5; 5 pages	<input type="checkbox"/>
37	A.F. Harvey, "DMA Fundamentals on Various PC Platforms," 1991, 1994, National Instruments Corporation, pp. 1-19; 19 pages	<input type="checkbox"/>
38	Application Number 12/058,586 (CD2007122): "System and Method for Monitoring a Target Device," Ogami et al., filed on 03/28/2008; 41 pages	<input type="checkbox"/>
39	Application Number 12/058,534 (CD2007123): "System and Method for Controlling a Target Device," Ogami et al., filed on 03/28/2008; 40 pages	<input type="checkbox"/>
40	Application Number 12/004,833 (CD2007124): "Systems and Methods for Dynamically Reconfiguring a Programmable System on a Chip," Ogami et al., filed on 12/21/2007; 40 pages	<input type="checkbox"/>
41	Application Number 12/058,569 (CD2007125): "Configuration of Programmable IC Design Elements," Best et al., filed on 03/28/2008; 19 pages	<input type="checkbox"/>
42	Application Number 12/057,149 (CD2007156): "Power Management Architecture, Method and Configuration System," Kenneth Ogami, filed on 03/27/2008; 34 pages	<input type="checkbox"/>
43	Application Number 12/765,400 (CD2009119): "Autonomous Control in a Programmable System," Sullam et al., filed on 04/22/2010; 30 pages	<input type="checkbox"/>
44	International Search Report for International Application Number PCT/US10/33626 (CD2009119PCT) mailed 06/24/2010; 3 pages	<input type="checkbox"/>

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45	The Written Opinion of the International Search Report for International Application Number PCT/US10/33626 (CD2009119PCT) mailed 06/24/2010; 5 pages	<input type="checkbox"/>
46	USPTO Non-Final Rejection for Application Number 12/060,176 (CD2007136) dated 03/30/2010; 22 pages	<input type="checkbox"/>
47	John Mangino, "Using DMA with High Performance Peripherals to Maximize System Performance," 2007, Texas Instruments, pp. 1-23; 23 pages	<input type="checkbox"/>
48	Balough et al., "White Paper: Comparing IP Integration Approaches for FPGA Implementation," February 2007, Version 1.1, Altera, pp. 1-7; 7 pages	<input type="checkbox"/>

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Attorney Docket Number	CD01213M

12	USPTO Non-Final Rejection for Application Number 11/644,100 (CD05204) dated 12/16/2009; 13 pages	<input type="checkbox"/>
13	USPTO Advisory Action for Application Number 11/644,100 (CD05204) dated 07/21/2010; 3 pages	<input type="checkbox"/>
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10001478
Filing Date	2001-11-01
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01213M

1	USPTO Advisory Action for Application Number 09/994,601 (CD01170) dated 03/23/2010; 2 pages	<input type="checkbox"/>
2	USPTO Non-Final Rejection for Application Number 11/818,005 (CD01171) dated 10/26/2010; 10 pages	<input type="checkbox"/>
3	USPTO Final Rejection for Application Number 12/132,527 (CD01175) dated 10/14/2010; 8 pages	<input type="checkbox"/>
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7	USPTO Non-Final Rejection for Application Number 09/989,767 (CD01178) dated 07/09/2010; 22 pages	<input type="checkbox"/>
8	USPTO Non-Final Rejection for Application Number 09/989,771 (CD01182) dated 07/20/2010; 12 pages	<input type="checkbox"/>
9	USPTO Advisory Action for Application Number 09/989,771 (CD01182) dated 02/03/2010; 3 pages	<input type="checkbox"/>
10	USPTO Miscellaneous Action for Application Number 10/113,581 (CD01188) dated 06/23/2010; 6 pages	<input type="checkbox"/>
11	USPTO Notice of Allowance for Application Number 10/113,581 (CD01188) dated 09/03/2010; 11 pages	<input type="checkbox"/>

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Application Number	10001478
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Attorney Docket Number	CD01213M

12	USPTO Advisory Action for Application Number 11/698,660 (CD05176) dated 11/10/2010; 3 pages	<input type="checkbox"/>
13	USPTO Final Rejection for Application Number 11/698,660 (CD05176) dated 09/03/2010; 19 pages	<input type="checkbox"/>
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